

Substitute Form PTO-1449 (Modified)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 10559-611001	Application No. 10/066,297
Information Disclosure Statement by Applicant (Use several sheets if necessary)		Applicant Gilbert Wolrich et al.	
		Filing Date February 1, 2002	Group Art Unit 2666

U.S. Patent Documents

Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
SH	AA	2001/0014100	08/2001	Abe et al.			
	AB	2002/0131443	09/2002	Robinett et al.			
	AC	2002/0144006	10/2002	Cranston et al.			
	AD	2002/0196778	12/2002	Colmant et al.			
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	AH	2004/0179533	09/2004	Donovan			
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SH	AAA	6,687,247	02/2004	Wilford et al.			

Examiner Signature

/Shick Hom/

Date Considered

7/27/2006

EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Substitute Form PTO-1449 (Modified) Information Disclosure Statement by Applicant (Use several sheets if necessary) 37 CFR § 1.962(b)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 10559-611001	Application No. 10/066,297
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	ACC	6,731,596	05/2004	Chiang et al.			
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Foreign Patent Documents or Published Foreign Patent Applications

Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
SH	ATT	0 418 447	03/1991	Europe	G06F	5/06		
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SH	AVV	WO 98/25210	06/1998	WIPO	G06F	15/16		
SH	AWW	WO 03/017541	02/2003	WIPO	H04L	12/54		

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**Information Disclosure Statement
by Applicant**

(Use several sheets if necessary)

Applicant

Gilbert Wolrich et al.

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Group Art Unit

2666

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Examiner Initial	Desig. ID	Document
SH	AXX	Adiletta, et al., "The next generation of Intel IXP Network Processors", Intel Technology Journal, Network Processors, vol. 6, issue 3, published Aug. 15, 2002, pp. 6-18.
	YYY	Brewer, et al., "Remote Queues: Exposing Message Queues for Optimization and Atomicity", SPAA '95 Santa Barbara, CA, pp. 1-13.
	AZZ	Buyuktosunoglu, A., et al., "Tradeoffs in Power-Efficient Issue Queue Design", ISLPED '02, ACM, Aug. 2002, 6 pages.
	AAAA	Dandamudi, S., "Multiprocessors", IEEE Computer, Mar. 1997, pp. 82-89.
	ABBB	Hendler, D., et al., "Work Dealing", SPAA '02, ACM, Aug. 2002, pp. 164-172.
	ACCC	Jonkers, H., "Queueing Models of Shared-Memory Parallel Applications", Computer and Telecommunications Systems Performance Engineering, Pentech Press, London, 1994, 13 pages.
	ADDD	Komaros, et al., "A Fully-Programmable Memory Management System Optimizing Queue Handling at Multi Gigabit Rates", ACM, Jun. 2-6, 2003, pp. 54-59.
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	AHHH	Michael, M., "Scalable Lock-Free Dynamic Memory Allocation", PLDI '04, ACM, Jun. 2004, pp. 1-12.
V	AIII	Pan, H., et al., "Heads and Tails: A Variable-Length Instruction Format Supporting Parallel Fetch and Decode", CASES 01, No. 16-17, 2001, 8 pages.
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Sheet 1 of 1

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SH	AA	5,649,092	07/1997	Price et al.	714	15	
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	AC						
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	AE						
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	AG						
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	AI						
	AJ						
	AK						

Foreign Patent Documents or Published Foreign Patent Applications								
Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
	AL						Yes	No
	AM							
	AN							
	AO							
	AP							

Other Documents (Include Author, Title, Date, and Place of Publication)		
Examiner Initial	Desig. ID	Document
	AQ	
	AR	
	AS	
	AT	

Examiner Signature /Shick Hom/	Not Considered 7/27/2006
<small>Unexamined documents submitted are hereby considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.</small>	

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